

LOGIC PROBE

MODEL LP-560

- TESTS ALL LOGIC FAMILIES (TTL, LSI, CMOS, ECT)
- CAPTURES PULSES TO 25ns (20MHz)
- AUDIO BEEPER, TWO SOUNDS HI & LOW
- MEMORY FUNCTION

SPECIFICATIONS

Working Voltage	4-18VDC
Frequency Response	20MHz
TTL: Logic "1" (HI LED)	>2.3 ±0.2VDC
Logic "0" (LO LED)	<0.8 ±0.2VDC
CMOS: Logic "1" (HI LED)	>70% Vcc ±10%
Logic "0" (LO LED)	<30% Vcc ±10%
Minimum Detectable Pulse Width	25ns
Input Impedance	1MΩ
Input Overload Protection	±220VAC/DC, 15s
Supply Voltage Protection	±20VDC
Pulse Indicator Flash Time	500ms
Operating Temperature	32° - 122°F

USER INSTRUCTIONS

1. Set the TTL/CMOS switch to the correct logic and the PULSE /MEM to PULSE.
2. Connect the red alligator clip to the positive (+) supply and the black alligator clip to the negative (-) supply of the test unit.
3. Touch the point to be tested with the probe tip and refer to the chart below:

INPUT SIGNAL	LED STATES			SOUND	
	HIGH	LO	PULSE		
	△	▽	○		Logic tip is not connected or bad logic level.
	△	▽	○	Low	Logic "0", no pulse activity.
	▲	▽	○	High	Logic "1", no pulse activity.
	△	▽	*	Low	Logic "0", with positive single pulses.
	▲	▽	*	High	Logic "1", with negative single pulses.
	▲	▽	*	Low/High	Square wave <200kHz.
	▲	▽	*	Low/High	Square wave >200kHz.

▲ - LED on △ - LED off * - LED Blinking ▴ - LED may be on or off

Memory Mode

1. Touch the point to be tested and set PULSE/MEM switch to MEM. The pulse LED lights with the first transition. The LED stays lit until its reset by switching to the PULSE position.



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